

# 512 Megabit Synchronous DRAM

## DP5D64MX8WY5

### DESCRIPTION:

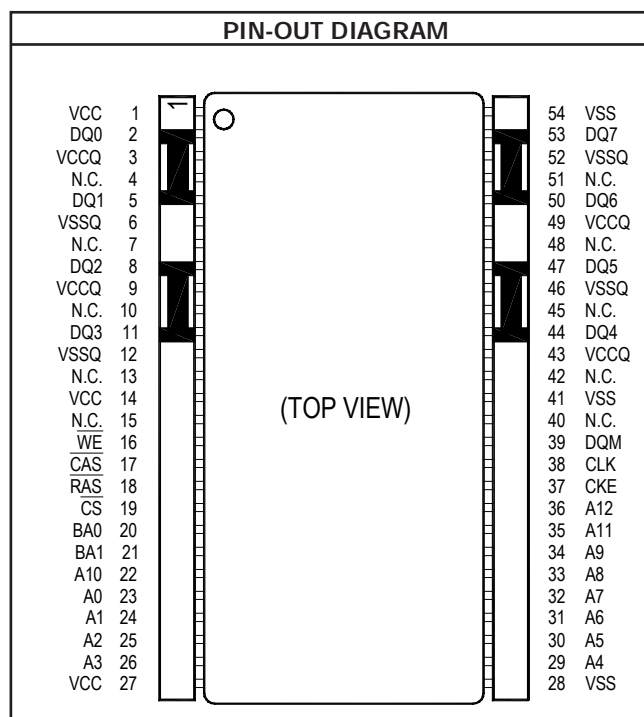
The LP-Stack™ series is a family of interchangeable memory modules. The 512 Megabit SDRAM is a member of this family which utilizes the new and innovative space saving TSOP stacking technology. The modules are constructed with 64 Meg x 4 SDRAMs.

This 256 Megabit based LP-Stack™ module, the DP5D64MX8WY5 has been designed to fit in the same footprint as the 32 Meg x 8 SDRAM TSOP monolithic and 64 and 128 Megabit SDRAM based family of LP-Stack™ modules.

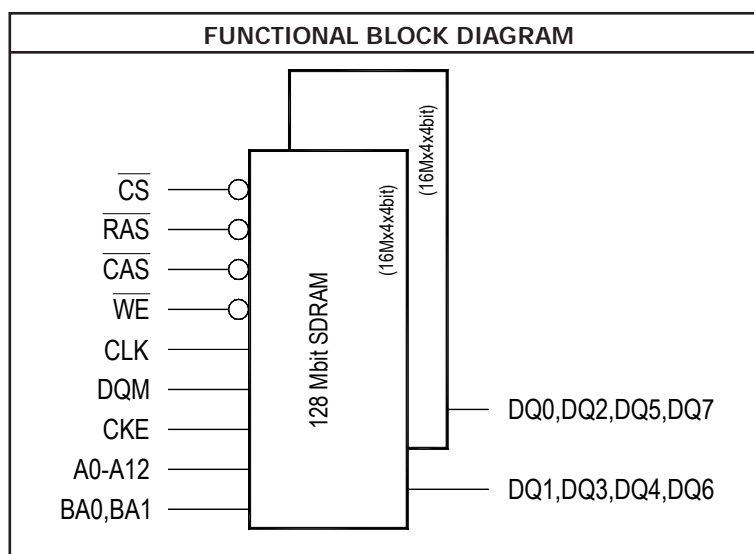
This allows the memory board designer to upgrade the memory in their products without redesigning the memory board, thus saving time and money.

### FEATURES:

- Configuration Available:  
64 Meg x 8 (16M x 4 x 8 bits)
- Clock Frequency:  
66, 83, 100, 125, 133 MHz (max.)
- PC100 and PC133 Compatible
- 3.3 Volt Power Supply
- LVTTTL Compatible I/O
- Four Bank Operation
- Programmable Burst Type, Burst Length, and CAS Latency
- 8192 Cycles / 64 ms
- Auto and Self Refresh
- Package: TSOP Leadless Stack



PIN NAMES	
A0-A12	Row Address: A0-A12 Column Address: A0-A9, A11
BA0,BA1	Bank Select Address
DQ0-DQ7	Data In/Data Out
CAS	Column Address Strobe
RAS	Row Address Enable
WE	Data Write Enable
DQM	Data Input/Output Mask
CKE	Clock Enable
CLK	System Clock
CS	Chip Select
Vcc/Vss	Power Supply/Ground
Vccq/Vssq	Data Output Power/Ground
N.C.	No Connect

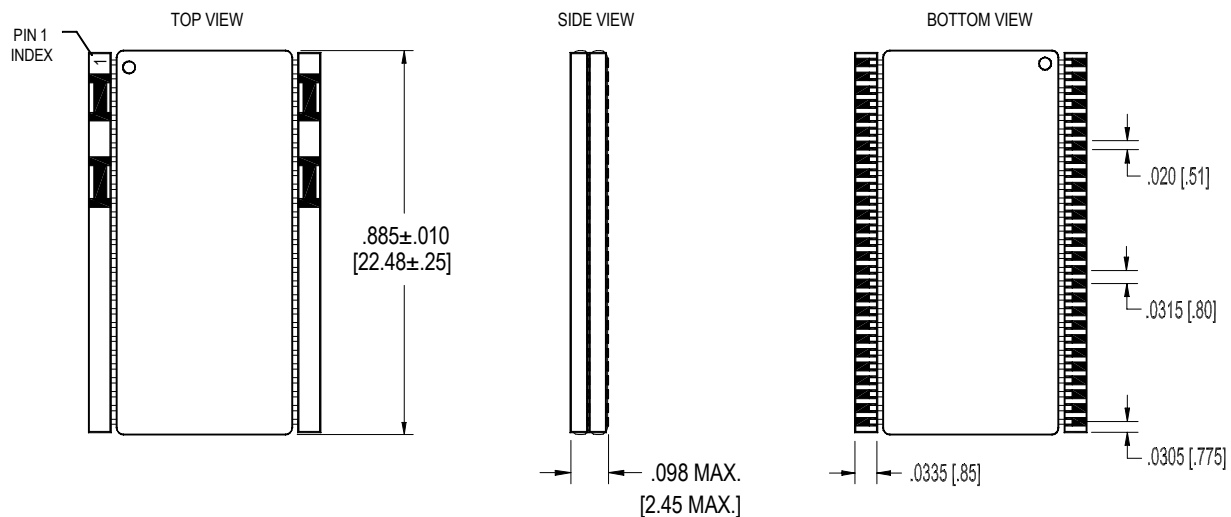


## PART NUMBER DESCRIPTION

DP PREFIX	SD TYPE	64M MEMORY DEPTH	X DESIG	8 MEMORY WIDTH	W DESIG	Y5 PACKAGE	- SUPPLIER	- XX MEMORY	XX SPEED	X CL	X GRADE	
												Blank Commercial Temperature
												2 CAS LATENCY 2
												15 15ns (66MHz)
												12 12ns (83MHz)
												10 10ns (100MHz)
												08 8ns (125MHz)
												75 7.5ns (133MHz)
												P1 PC100
												MANUFACTURER CODE *
												SUPPLIER CODE *
												STACKABLE TSOP
												256 MEGABIT LVTTL BASED
												MEMORY MODULE WITHOUT SUPPORT LOGIC
												SYNCHRONOUS DRAM

\* Contact your sales representative for supplier and manufacturer codes.

## MECHANICAL DRAWING



Standard TSOP pad layout is acceptable, however, when possible, the following pad layout is recommended for optimal manufacture and inspection. See Application Note 53A001-00 for further information.

