

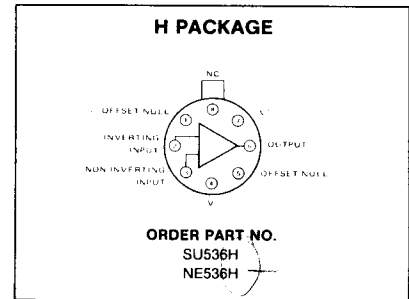
DESCRIPTION

The 536 is a special purpose high performance operational amplifier utilizing an FET input stage for extremely high input impedance and low input current.

The device features internal compensation, standard pinout, wide differential and common mode input voltage range, high slew rate and high output drive capability.

FEATURES

- 5pA input bias current
- Input and output protection
- Offset null capability
- Internally compensated
- 6V/ μ sec slew rate
- Standard pinout
- 1MHz unity gain bandwidth

PIN CONFIGURATION**ABSOLUTE MAXIMUM RATINGS**

PARAMETER	RATING	UNIT
Supply voltage	± 22	V
Differential input voltage range	± 30	V
Common mode input voltage range	$\pm V_s$	
Power dissipation ¹	500	mW
Operating temperature range		
SU536T	-55 to +85	$^{\circ}$ C
NE536T	0 to +70	$^{\circ}$ C
Storage temperature range	-65 to +150	$^{\circ}$ C
Lead temperature (solder, 60sec)	300	$^{\circ}$ C
Output short circuit duration ²	indefinite	

NOTES

1. Rating applies for case temperature to +25 $^{\circ}$ C; derate linearly at 6.5mW/ $^{\circ}$ C for ambient temperatures above 75 $^{\circ}$ C.
2. Short circuit may be to ground or either supply. Rating applies to +125 $^{\circ}$ C case temperature or +75 $^{\circ}$ C ambient temperature.

DC ELECTRICAL CHARACTERISTICS $T_A = 25^{\circ}\text{C}$, $V_s = \pm 15\text{V}$ unless otherwise specified.¹

PARAMETER	TEST CONDITIONS	NE536			UNIT
		Min	Typ	Max	
V_{OS} Offset voltage	$R_S \leq 10\text{k}\Omega$		30	90	mV
V_{OS} Drift	Over temp., $R_S \leq 10\text{k}\Omega$ $R_S = 0\Omega$, over temp.		30 30		mV $\mu\text{V}/^{\circ}\text{C}$
I_{OS} Offset current			5		pA
I_{BIAS} Input current ²			30	100	pA
V_{CM} Common mode voltage range		± 10	± 11		V
CMRR Common mode rejection ratio	$R_S \leq 10\text{k}\Omega$, $V_{IN} = \pm 10\text{V}$	64	80		dB
R_{IN} Input resistance			10^{14}		Ω
V_{OUT} Output voltage swing	$R_L \geq 2\text{k}\Omega$, over temp. $R_L 10\text{k}\Omega$, over temp.	± 10 ± 12	± 11 ± 13		V V
I_{CC} Supply current	$V_{OUT} = 0\text{V}$		6.0	8.0	mA
PSRR Supply voltage rejection ratio	$R_S \leq 10\text{k}\Omega$, $\pm 6 \leq V_s \leq 15$		100	300	$\mu\text{V}/\text{V}$
A_{VOL} Large signal voltage gain	$V_O = \pm 10\text{V}$, $R_L 2\text{k}\Omega$ $V_O = \pm 10\text{V}$, $R_L \geq 2\text{k}\Omega$, over temp.	50 25			V/mV V/mV
P_s Power supply range		± 6	± 18		V

NOTES

1. Operating temperature range: NE536 is 0 $^{\circ}$ C to 70 $^{\circ}$ C.
2. Input current typically doubles every 10 $^{\circ}$ C.

DC ELECTRICAL CHARACTERISTICS $T_A = 25^\circ\text{C}$, $\pm 6\text{V} \leq V_S \leq \pm 20\text{V}$ unless otherwise specified.²

PARAMETER	TEST CONDITIONS	SU536			UNIT
		Min	Typ	Max	
V_{OS} Offset voltage	$R_S \leq 10\text{k}\Omega$ $R_S \leq 10\text{k}\Omega$, over temp.		7.5 7.5	20 30	mV mV
V_{OS} Drift	$R_S \leq 10\text{k}\Omega$		20		$\mu\text{V}/^\circ\text{C}$
I_{OS} Offset current			5		pA
I_{BIAS} Input current ¹	Over temp.		5 250	30 3000	pA pA
V_{CM} Common mode voltage range	$V_S = \pm 15\text{V}$	± 10	± 11		V
CMRR Common mode rejection ratio	$R_S \leq 10\text{k}\Omega$, $V_{IN} = \pm 10\text{V}$	70	80		dB
R_{IN} Input resistance			10 ¹⁴		Ω
V_{OUT} Output voltage swing	$R_L \geq 2\text{k}\Omega$, $V_S = \pm 15\text{V}$, over temp. $R_L \geq 10\text{k}\Omega$, $V_S = \pm 15\text{V}$, over temp.	± 10 ± 12	± 12 ± 13		V V
I_{CC} Supply current	$V_{OUT} = 0\text{V}$, $V_S = \pm 20\text{V}$		6.0	8.0	mA
P_{SRR} Supply voltage rejection ratio	$R_S \leq 10\text{k}\Omega$		50	150	$\mu\text{V}/\text{V}$
A_{VOL} Large signal voltage gain	Over temp., $V_S = \pm 15\text{V}$, $V_O = \pm 10\text{V}$, $R_L \geq 2\text{k}\Omega$	50			V/mV
P_S Power supply range		± 6		± 20	V

NOTES

1. Input current typically doubles every 10°C .
2. Operating temperature range for SU536 is -55°C to $+85^\circ\text{C}$.

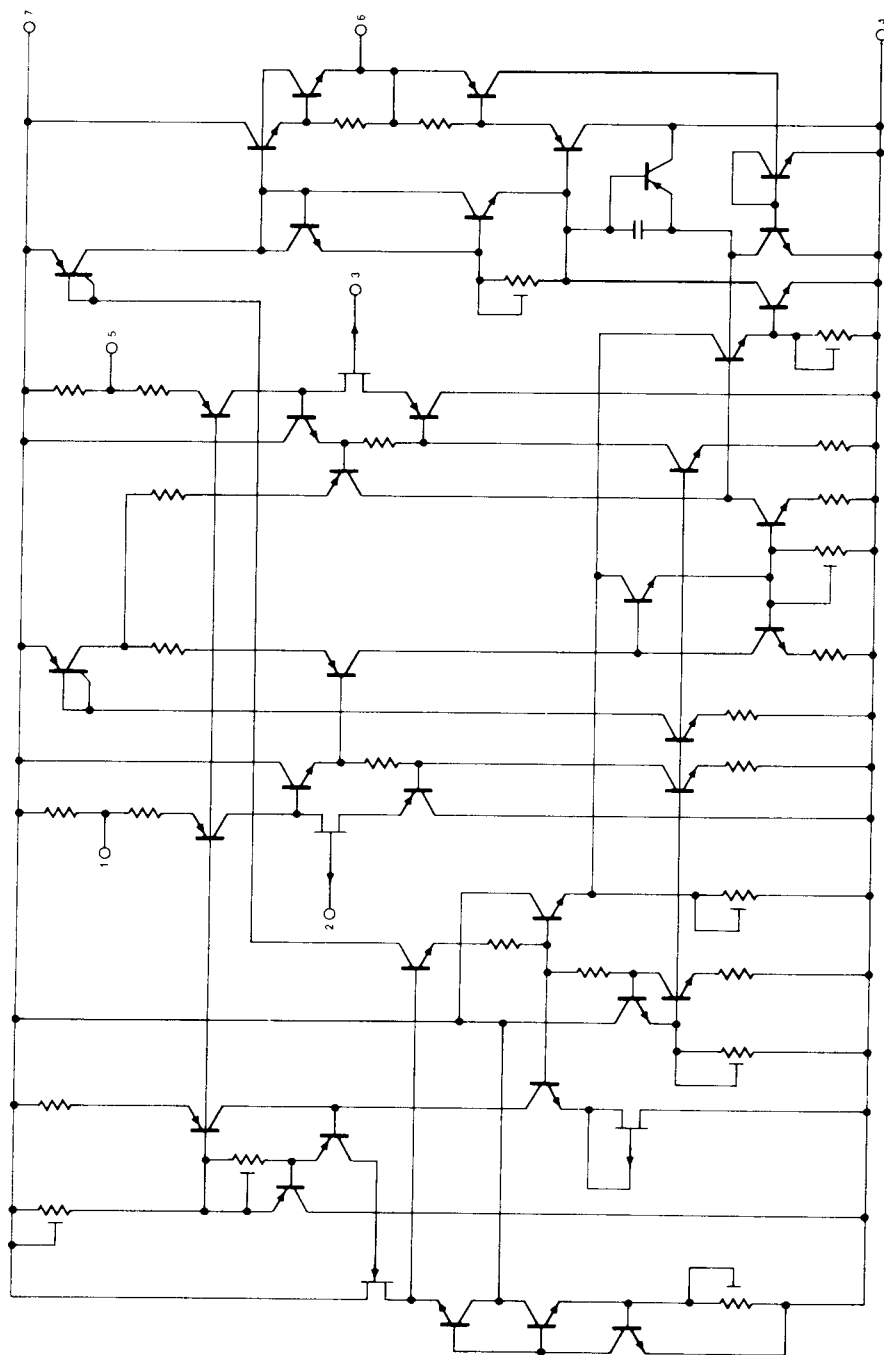
AC ELECTRICAL CHARACTERISTICS $T_A = 25^\circ\text{C}$ unless otherwise specified.^{1,2}

PARAMETER	TEST CONDITIONS	NE536			SU536			UNIT
		Min	Typ	Max	Min	Typ	Max	
Differential capacitance			6			6		pF
Input noise voltage	0.1Hz — 100kHz		20			20		μV_{rms}
Output impedance			100			100		
Unity gain frequency	$V_S = \pm 15\text{V}$		1			1		MHz
Full power bandwidth	$V_S = \pm 15\text{V}$		100			100		KHz
Slew rate, inverter	$V_S = \pm 15\text{V}$, $A = -1\text{V}$		6			6		$\text{V}/\mu\text{s}$
Slew rate, follower	$V_S = \pm 15\text{V}$, $A = +1\text{V}$		6			6		$\text{V}/\mu\text{s}$

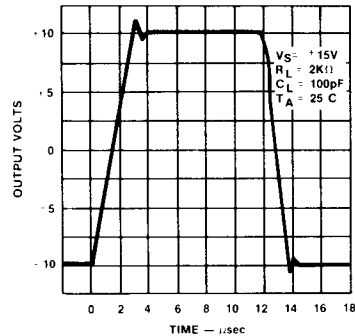
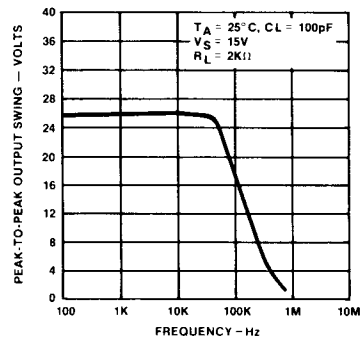
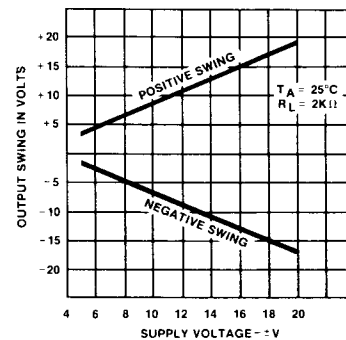
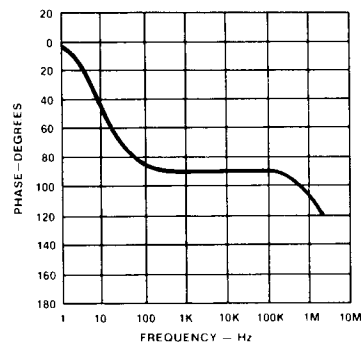
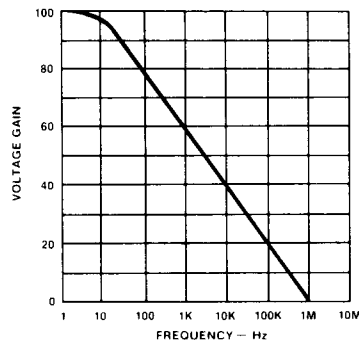
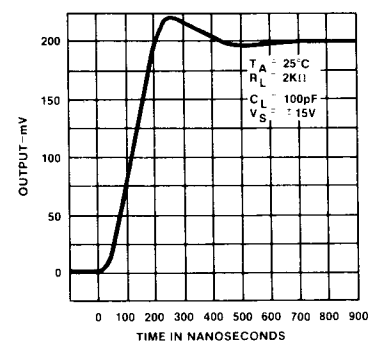
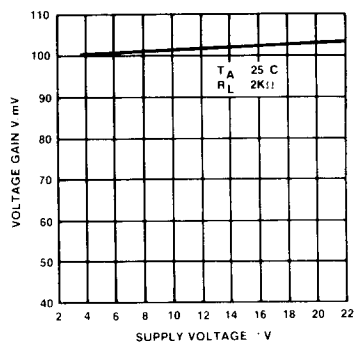
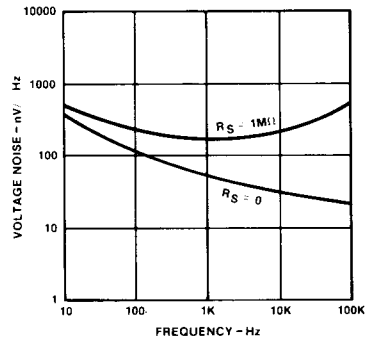
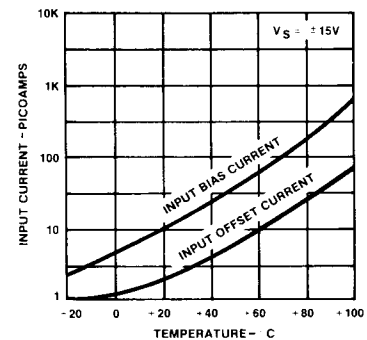
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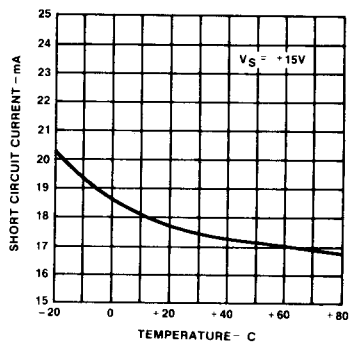
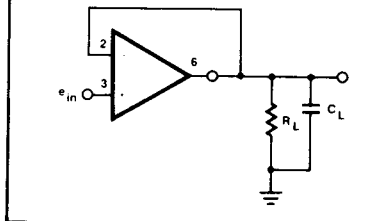
1. Temperature range for SU536 is $-55 \leq T_A \leq 85^\circ\text{C}$
Temperature range for NE536 is $0^\circ\text{C} \leq T_A \leq 70^\circ\text{C}$
2. SU536 — $\pm 6\text{V} \leq T_A \leq 20\text{V}$
NE536 — $\pm 15\text{V}$

CIRCUIT SCHEMATIC



TYPICAL PERFORMANCE CHARACTERISTICS

LARGE SIGNAL VOLTAGE FOLLOWER
PULSE RESPONSEOUTPUT VOLTAGE SWING AS A
FUNCTION OF FREQUENCYOUTPUT VOLTAGE SWING AS A
FUNCTION OF SUPPLY VOLTAGEOPEN LOOP PHASE RESPONSE AS A
FUNCTION OF FREQUENCYOPEN LOOP VOLTAGE GAIN AS A
FUNCTION OF FREQUENCYVOLTAGE FOLLOWER
TRANSIENT RESPONSEOPEN LOOP VOLTAGE GAIN AS A
FUNCTION OF SUPPLY VOLTAGEINPUT VOLTAGE NOISE AS A
FUNCTION OF FREQUENCYINPUT CURRENTS AS A FUNCTION
OF AMBIENT TEMPERATURE

**TYPICAL PERFORMANCE
CHARACTERISTICS** (Cont'd)**OUTPUT SHORT-CIRCUIT CURRENT
AS A FUNCTION OF
AMBIENT TEMPERATURE****TEST CIRCUITS****VOLTAGE FOLLOWER CIRCUIT****OFFSET NULL CIRCUIT**