

OUTLINE

The R1124N Series are CMOS-based voltage regulator ICs with high output voltage accuracy, extremely low supply current, low ON-resistance, and high ripple rejection. Each of these ICs consists of a voltage reference unit, an error amplifier, resistor-net for voltage setting, a current limit circuit, and a chip enable circuit.

These ICs perform with low dropout voltage and a chip enable function. The line transient response and load transient response of the R1124N Series are excellent, thus these ICs are very suitable for the power supply for hand-held communication equipment.

The output voltage of these ICs is fixed with high accuracy. Since the package for these ICs is SOT-23-5, high density mounting of the ICs on boards is possible.

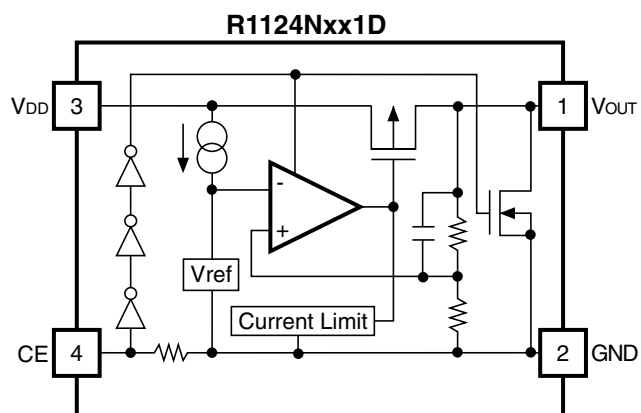
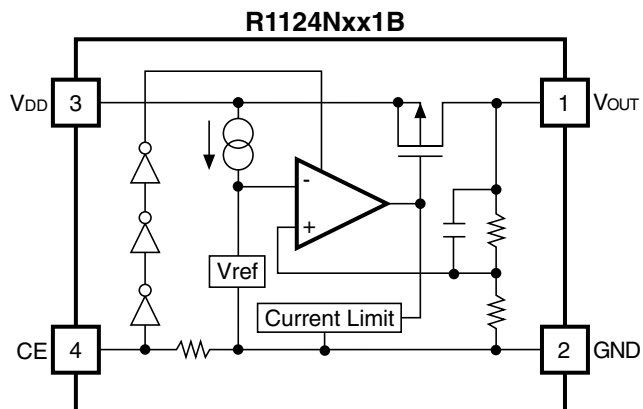
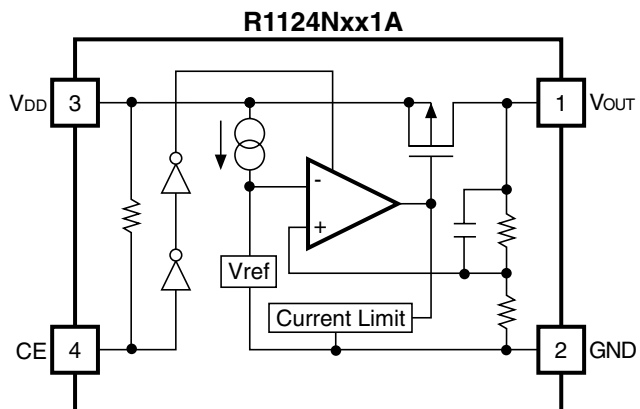
FEATURES

- Low Supply CurrentTyp. 75 μ A
- Standby ModeTyp. 0.1 μ A
- Low Dropout VoltageTyp. 0.22V ($I_{OUT}=150\text{mA}$ 3.0V Output type)
- High Ripple RejectionTyp. 70dB ($f=1\text{kHz}$ 3.0V Output type)
Typ. 60dB ($f=10\text{kHz}$)
- Low Temperature-Drift Coefficient of Output Voltage.....Typ. $\pm 100\text{ppm}/^{\circ}\text{C}$
- Excellent Line Regulation.....Typ. 0.02%/V
- High Output Voltage Accuracy $\pm 2.0\%$
- Small PackageSOT-23-5
- Output Voltage.....Stepwise setting with a step of 0.1V in the range of 1.5V to 4.0V is possible
- Built-in Fold Back Protection Circuit.....Typ. 40mA (Current at short mode)
- Ceramic capacitors are recommended to be used with this IC $C_{IN}=C_{OUT}=1\mu\text{F}$ ($V_{OUT}<2.5\text{V}$)
 $C_{IN}=1\mu\text{F}$, $C_{OUT}=0.47\mu\text{F}$ ($V_{OUT}\geq 2.5\text{V}$)

APPLICATIONS

- Power source for portable communication equipment.
- Power source for electrical appliances such as cameras, VCRs and camcorders.
- Power source for battery-powered equipment.

BLOCK DIAGRAMS



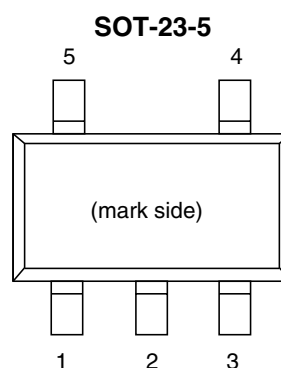
SELECTION GUIDE

The output voltage, version, and the taping type for the ICs can be selected at the user's request. The selection can be made with designating the part number as shown below;

R1124Nxx1x-xx ←Part Number
 ↑ ↑ ↑
 a b c

Code	Contents
a	Setting Output Voltage (V_{OUT}): Stepwise setting with a step of 0.1V in the range of 1.5V to 4.0V is possible.
b	Designation of Active Type: A: active low type B: active high type D: active high, with auto discharge
c	Designation of Taping Type: Ex. TR (refer to Taping Specifications; TR type is the standard direction.)

PIN CONFIGURATION



PIN DESCRIPTIONS

Pin No.	Symbol	Description
1	V_{OUT}	Output pin
2	GND	Ground Pin
3	V_{DD}	Input Pin
4	\overline{CE} or CE	Chip Enable Pin
5	NC	No Connection

ABSOLUTE MAXIMUM RATINGS

Symbol	Item	Rating	Unit
V_{IN}	Input Voltage	6.5	V
V_{CE}	Input Voltage (\overline{CE} or CE Pin)	$-0.3 \sim V_{IN} + 0.3$	V
V_{OUT}	Output Voltage	$-0.3 \sim V_{IN} + 0.3$	V
I_{OUT}	Output Current	200	mA
P_D	Power Dissipation	250	mW
T_{opt}	Operating Temperature Range	$-40 \sim 85$	°C
T_{stg}	Storage Temperature Range	$-55 \sim 125$	°C

ELECTRICAL CHARACTERISTICS

• R1124Nxx1x

T_{opt} = 25°C

Symbol	Item	Conditions	Min.	Typ.	Max.	Unit
V _{OUT}	Output Voltage	V _{IN} = Set V _{OUT} + 1V 1mA ≤ I _{OUT} ≤ 30mA	V _{OUT} × 0.980		V _{OUT} × 1.020	V
I _{OUT}	Output Current	V _{IN} - V _{OUT} = 1.0V	150			mA
ΔV _{OUT} /ΔI _{OUT}	Load Regulation	V _{IN} = Set V _{OUT} + 1V 1mA ≤ I _{OUT} ≤ 150mA		22	40	mV
V _{DIF}	Dropout Voltage	Refer to the ELECTRICAL CHARACTERISTICS by OUTPUT VOLTAGE				
I _{SS}	Supply Current	V _{IN} = Set V _{OUT} + 1V, I _{OUT} = 0mA		75	95	μA
I _{standby}	Supply Current (Standby)	V _{IN} = Set V _{OUT} + 1V V _{CE} = GND (B/D version) = V _{DD} (A version)		0.1	1.0	μA
ΔV _{OUT} /ΔV _{IN}	Line Regulation	V _{OUT} > 1.7V, Set V _{OUT} + 0.5V ≤ V _{IN} ≤ 6.0V (V _{OUT} ≤ 1.7V, 2.2V ≤ V _{IN} ≤ 6.0V) I _{OUT} = 30mA		0.02	0.10	%/V
RR	Ripple Rejection	f = 1kHz f = 10kHz Ripple 0.5Vp-p V _{OUT} > 1.7V, V _{IN} - V _{OUT} = 1.0V V _{OUT} ≤ 1.7, V _{IN} - V _{OUT} = 1.2V I _{OUT} = 30mA		70 60		dB
V _{IN}	Input Voltage		2.0		6.0	V
ΔV _{OUT} /ΔT	Output Voltage Temperature Coefficient	I _{OUT} = 30mA -40°C ≤ T _{opt} ≤ 85°C		±100		ppm /°C
I _{LIM}	Short Current Limit	V _{OUT} = 0V		40		mA
R _{PU}	CE Pull-up Resistance		0.7	2.0	8.0	MΩ
V _{CEH}	CE Input Voltage "H"		1.5		V _{IN}	V
V _{CEL}	CE Input Voltage "L"		0.0		0.3	V
en	Output Noise	BW = 10Hz to 100kHz		30		μV _{rms}
R _{LOW}	On Resistance of Nch for auto-discharge (Only for D version)	V _{CE} = 0V		60		Ω

• ELECTRICAL CHARACTERISTICS by OUTPUT VOLTAGE

 $T_{opt} = 25^{\circ}\text{C}$

Output Voltage V_{OUT} (V)	Dropout Voltage		
	V_{DIF} (V)		
	Condition	Typ.	Max.
$V_{OUT} = 1.5$	$I_{OUT} = 150\text{mA}$	0.38	0.70
$V_{OUT} = 1.6$		0.36	0.65
$V_{OUT} = 1.7$		0.34	0.60
$1.8 \leq V_{OUT} \leq 2.0$		0.32	0.55
$2.1 \leq V_{OUT} \leq 2.7$		0.28	0.50
$2.8 \leq V_{OUT} \leq 4.0$		0.22	0.35

TECHNICAL NOTES

When using these ICs, consider the following points:

Phase Compensation

In these ICs, phase compensation is made for securing stable operation even if the load current is varied. For this purpose, use a capacitor C_{OUT} with good frequency characteristics and ESR (Equivalent Series Resistance).

(Note: When the additional ceramic capacitors are connected to the output pin with an output capacitor for phase compensation, the operation might be unstable. Because of this, test these ICs with as same external components as ones to be used on the PCB.)

PCB Layout

Make V_{DD} and GND lines sufficient. If their impedance is high, noise pickup or unstable operation may result. Connect a capacitor with a capacitance value as much as $1.0\mu\text{F}$ or more between V_{DD} and GND pin, and as close as possible to the pins.

Set external components, especially the output capacitor, as close as possible to the ICs, and make wiring as short as possible.

TEST CIRCUITS

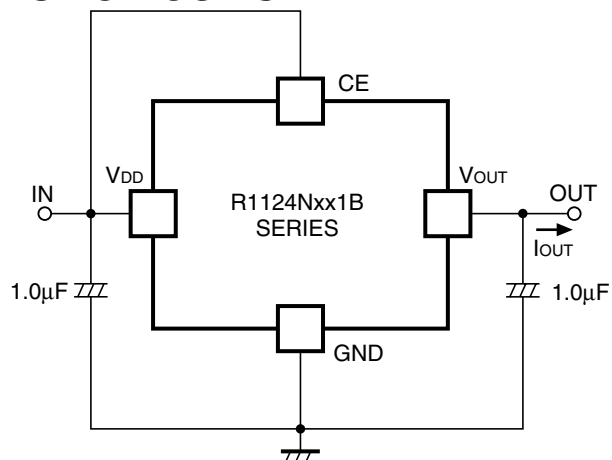


Fig.1 Standard test Circuit

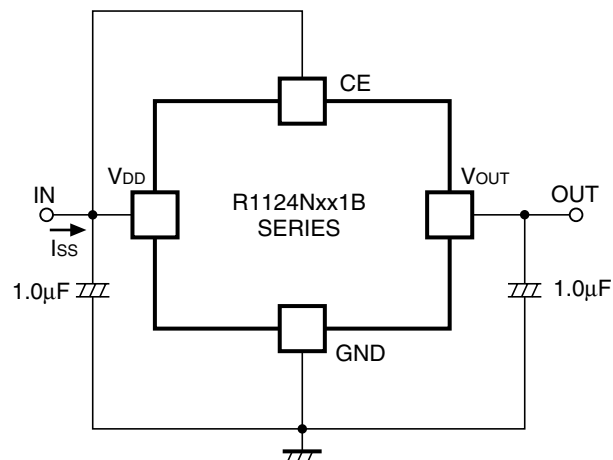


Fig.2 Supply Current Test Circuit

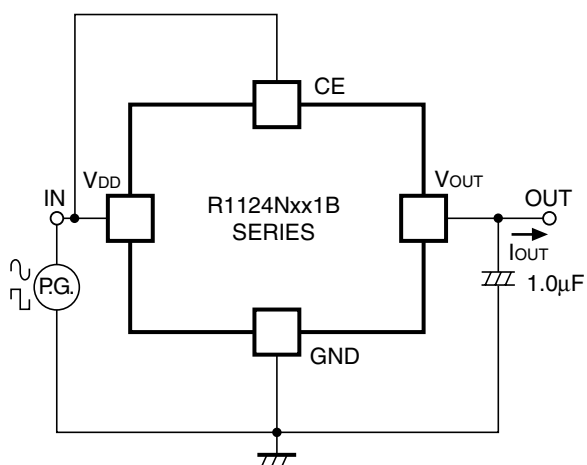


Fig.3 Ripple Rejection, Line Transient Response Test Circuit

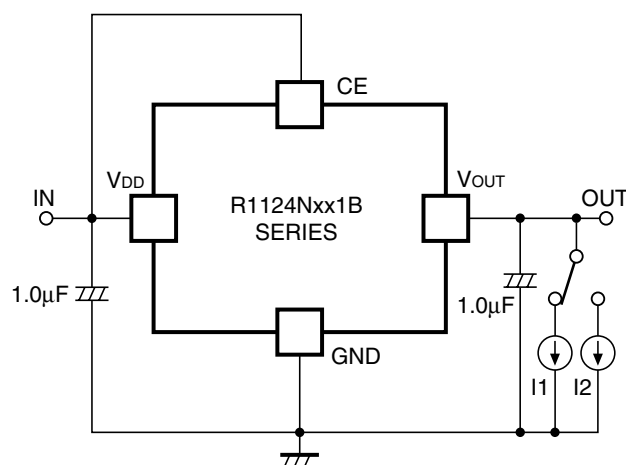
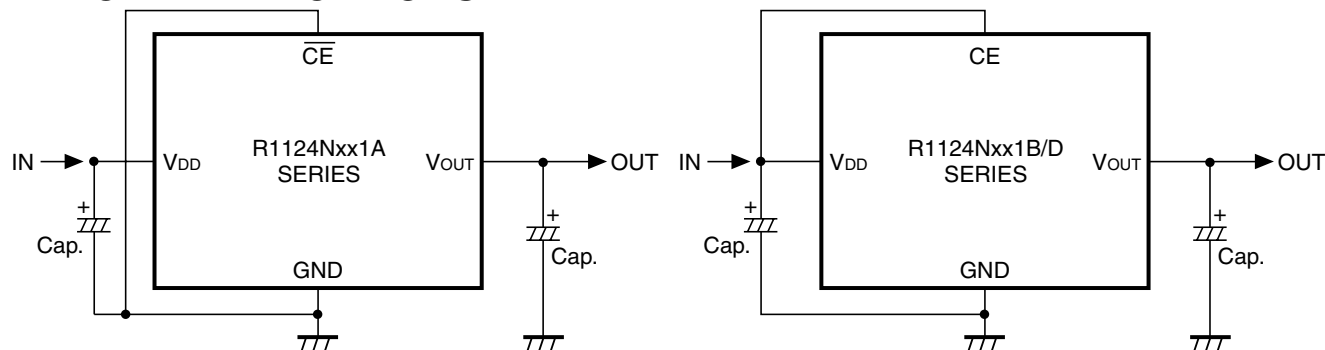


Fig.4 Load Transient Response Test Circuit

TYPICAL APPLICATIONS



(External Components)

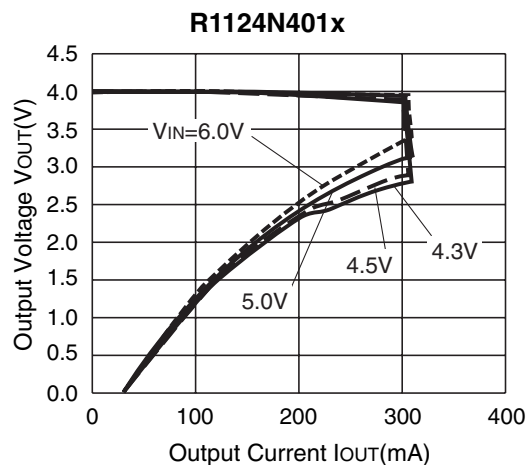
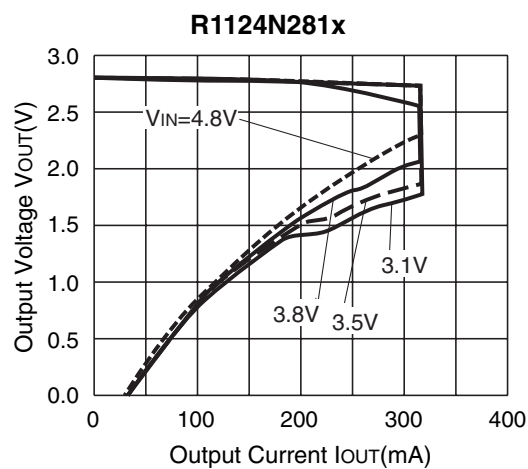
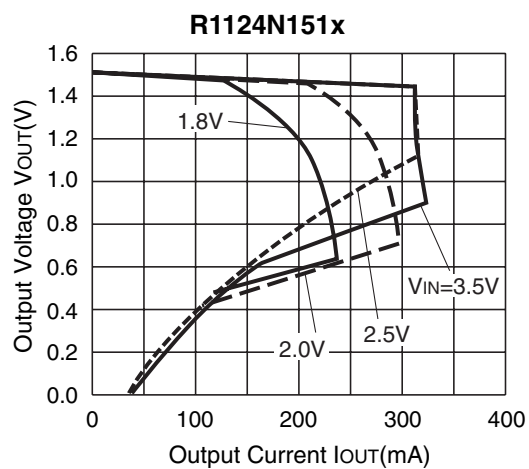
Output Capacitor; Ceramic $0.47\mu\text{F}$ (Set Output Voltage in the range from 2.5 to 5.0V)

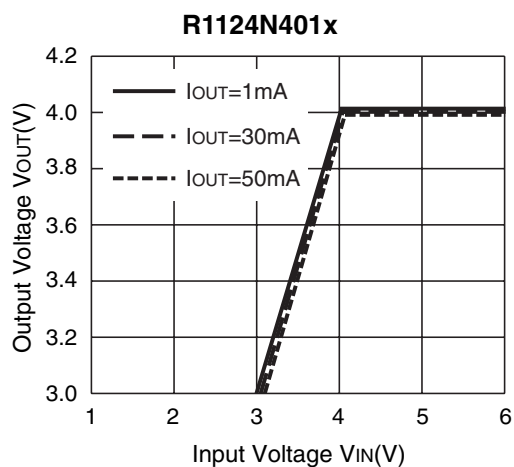
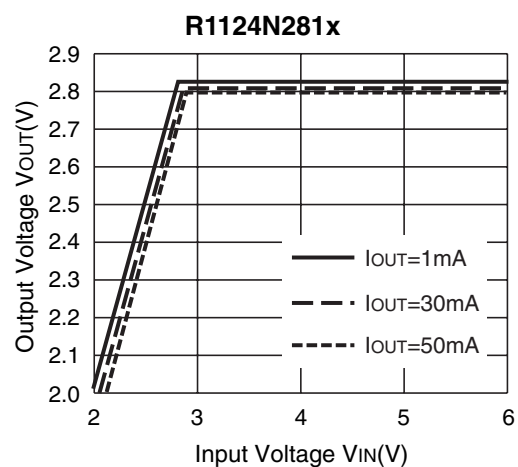
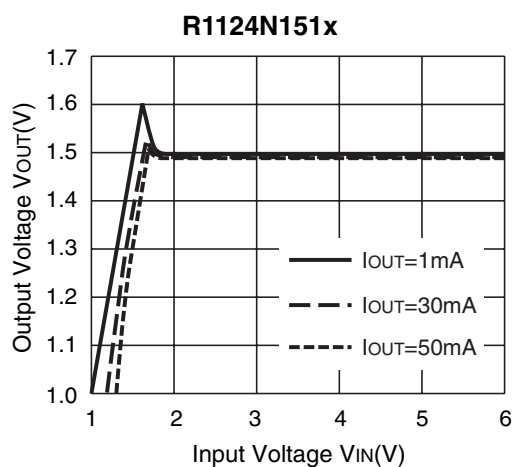
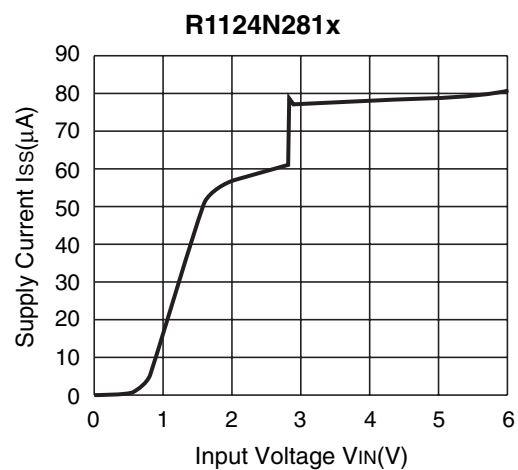
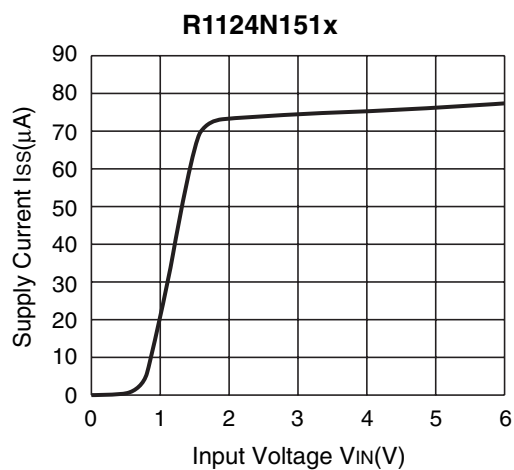
Ceramic $1.0\mu\text{F}$ (Set Output Voltage in the range from 2.0 to 2.4V)

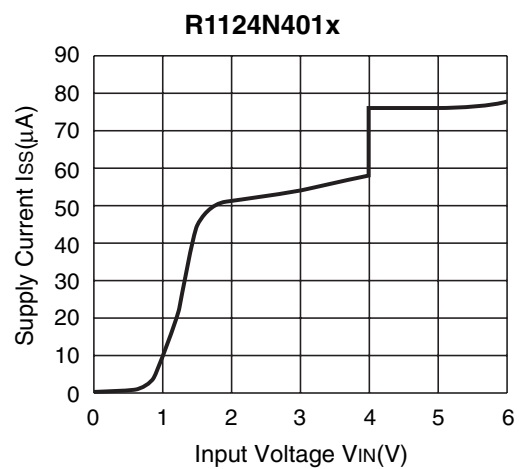
Input Capacitor; Ceramic $1.0\mu\text{F}$

TYPICAL CHARACTERISTICS

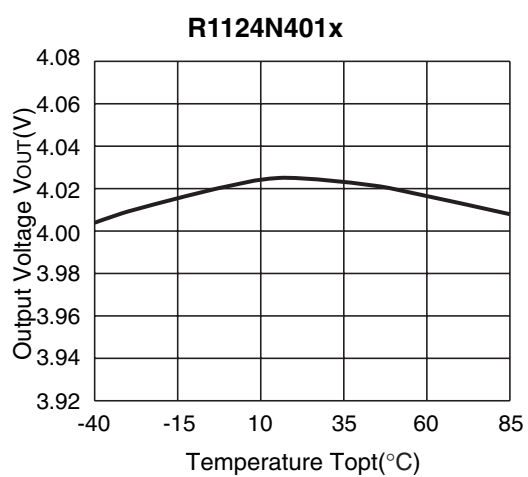
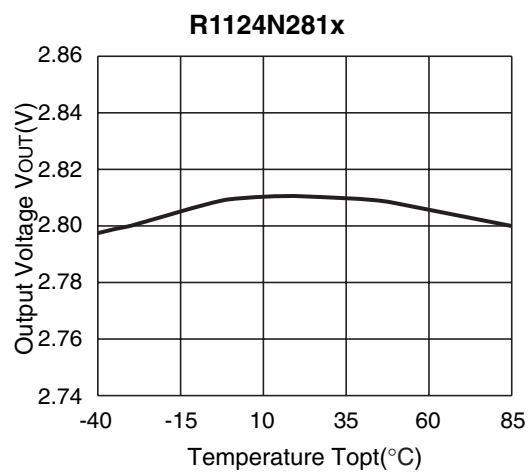
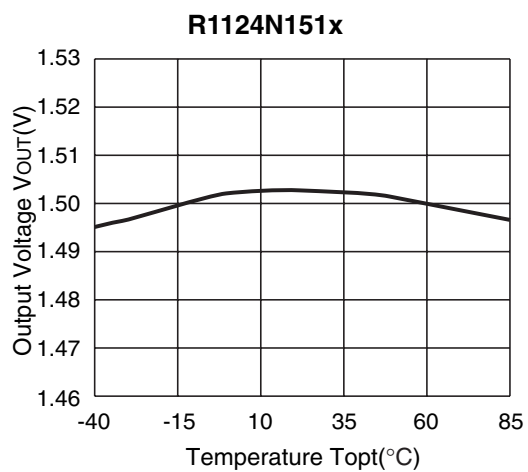
1) Output Voltage vs. Output Current ($T_{\text{opt}}=25^\circ\text{C}$)



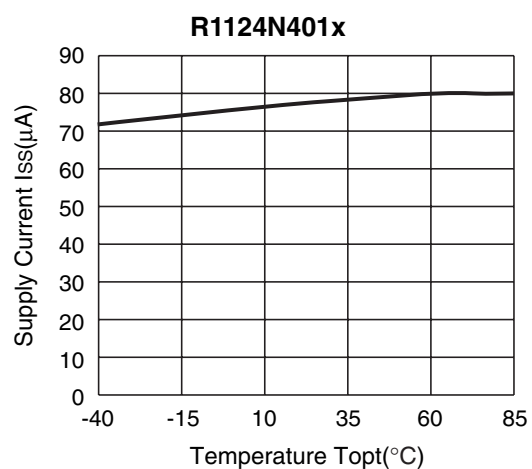
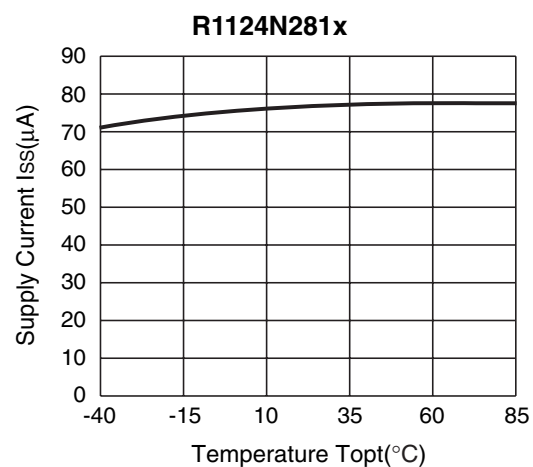
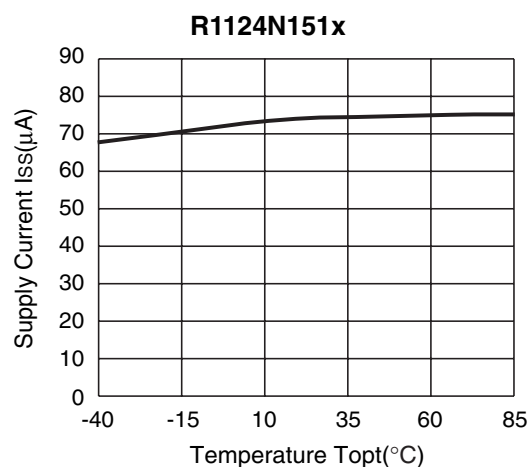
2) Output Voltage vs. Input Voltage ($T_{opt}=25^{\circ}\text{C}$)3) Supply Current vs. Input Voltage ($T_{opt}=25^{\circ}\text{C}$)



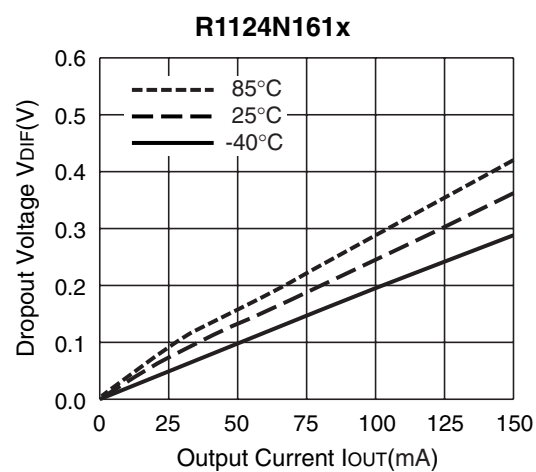
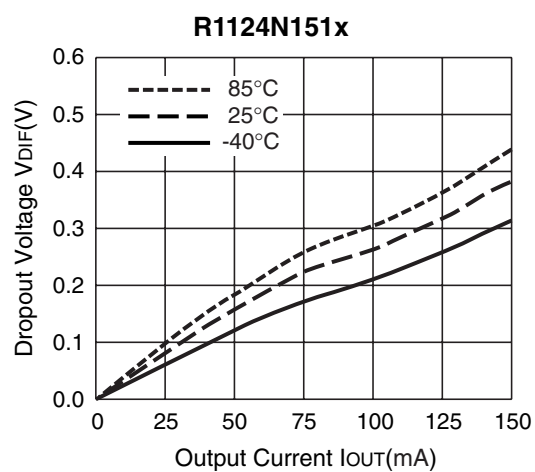
4) Output Voltage vs. Temperature

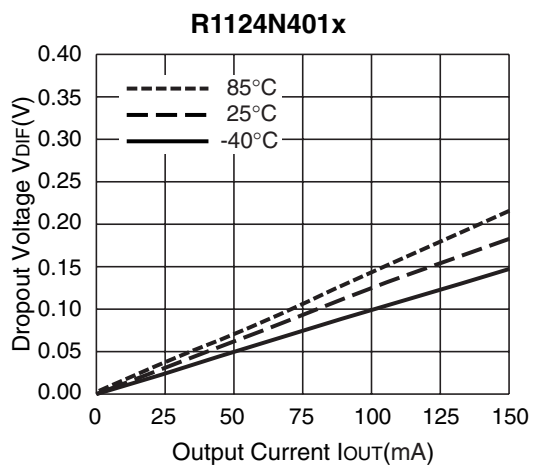
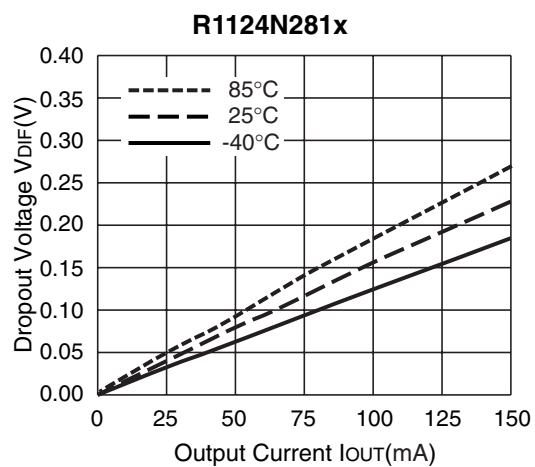
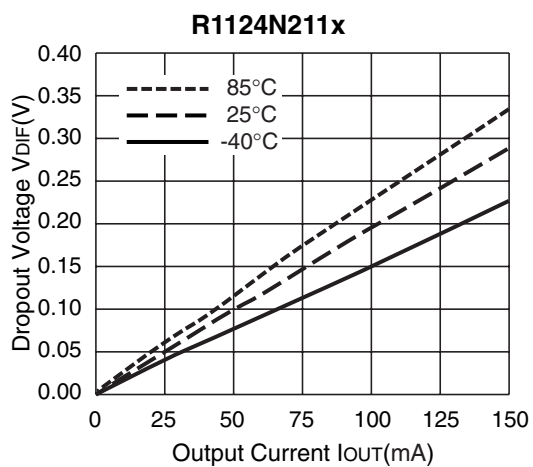
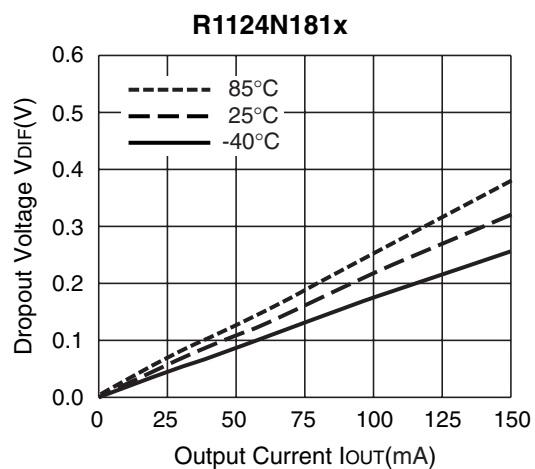
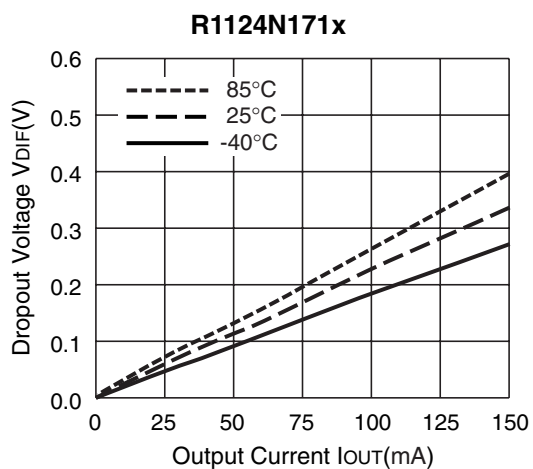


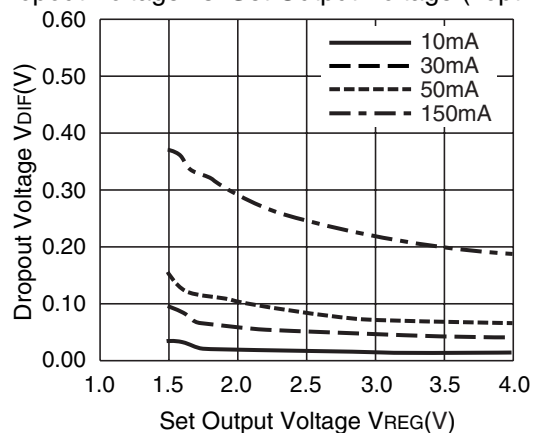
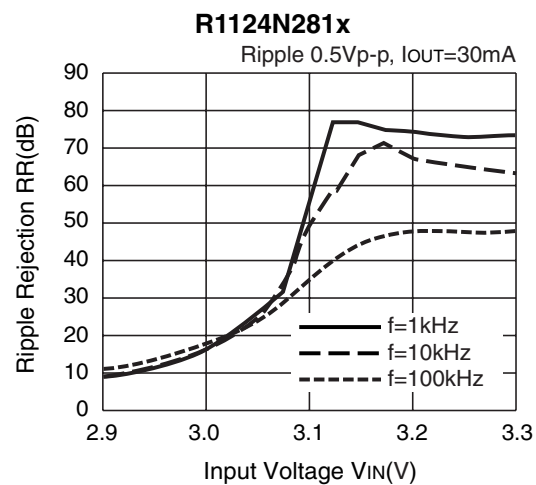
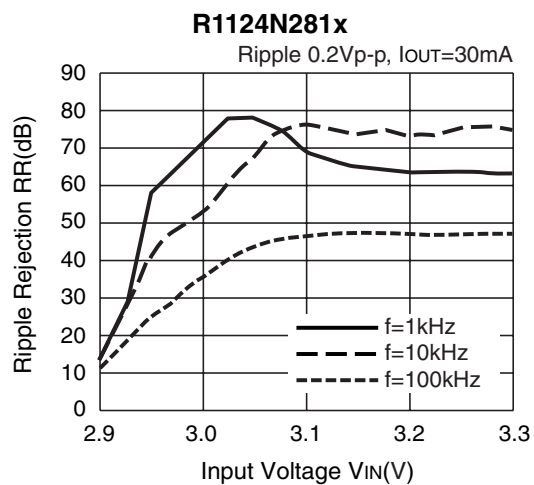
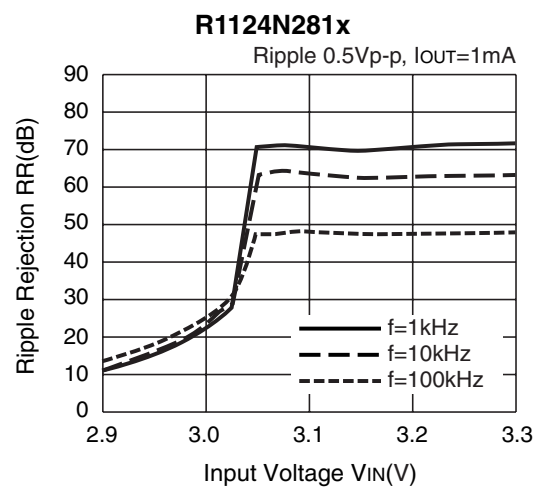
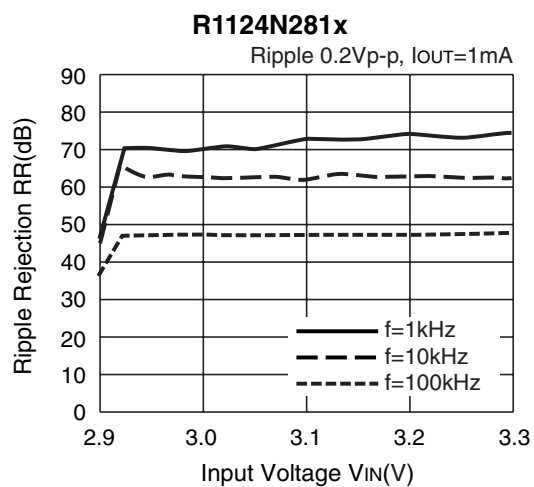
5) Supply Current vs. Temperature

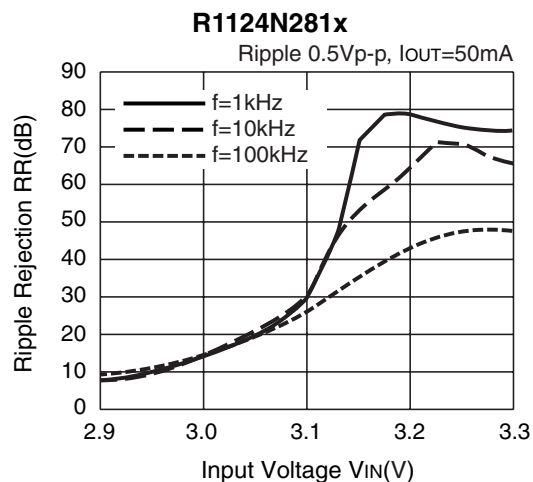
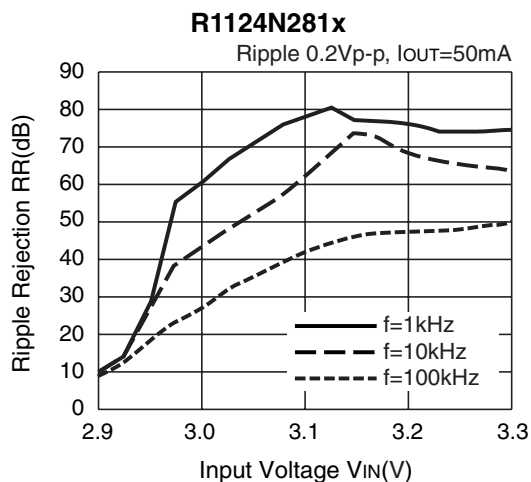


6) Dropout Voltage vs. Temperature

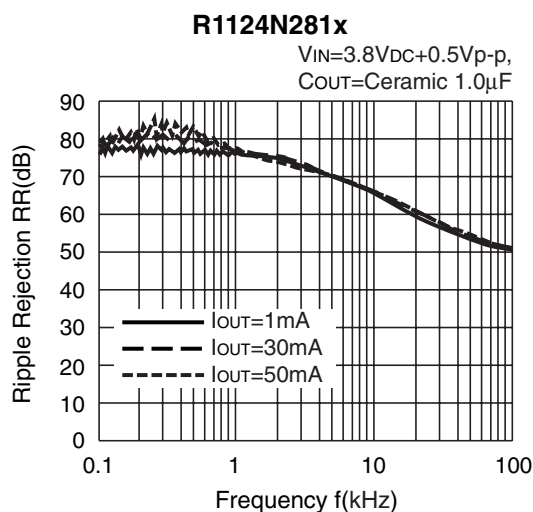
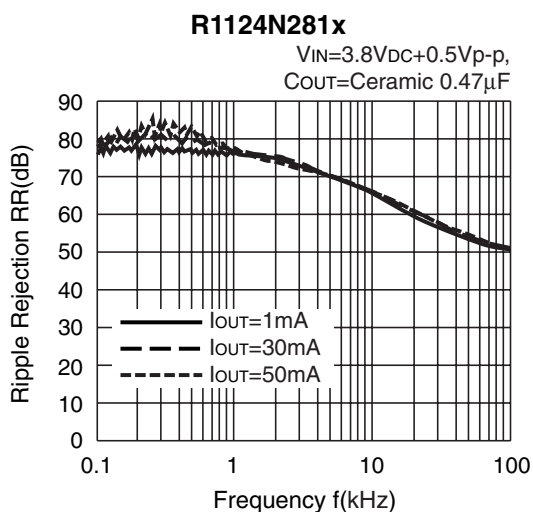
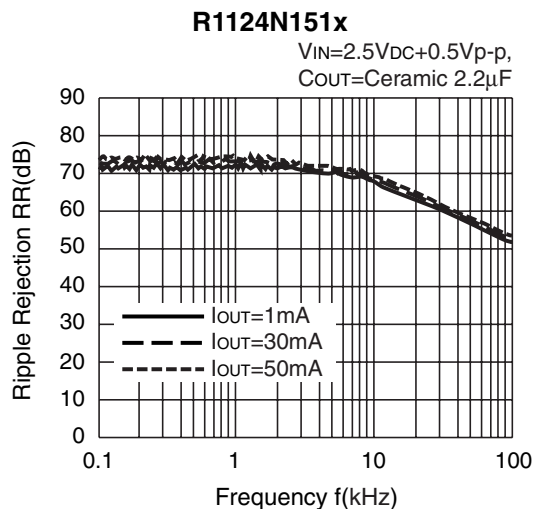
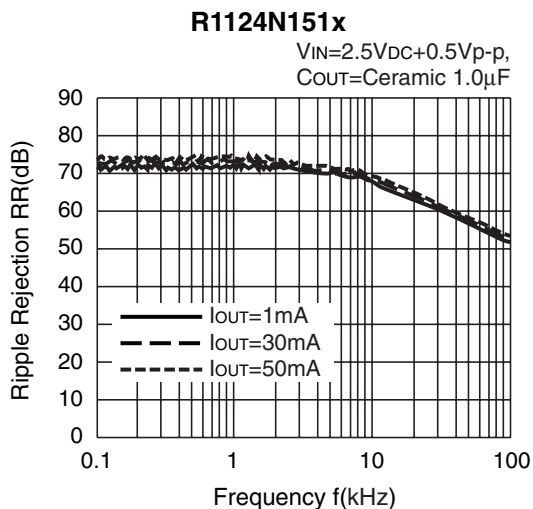


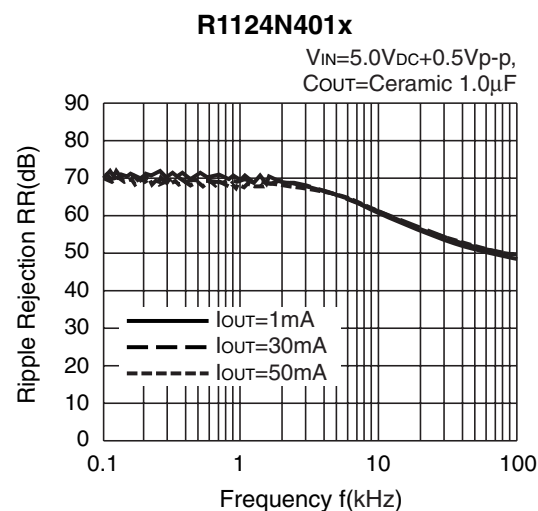
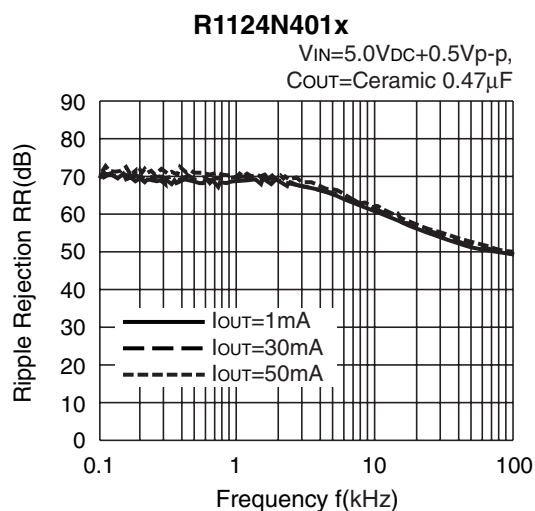


7) Dropout Voltage vs. Set Output Voltage (T_{opt}=25°C)8) Ripple Rejection vs. Input Bias Voltage (T_{opt}=25°C, C_{IN}=none, C_{OUT}=ceramic0.47μF)

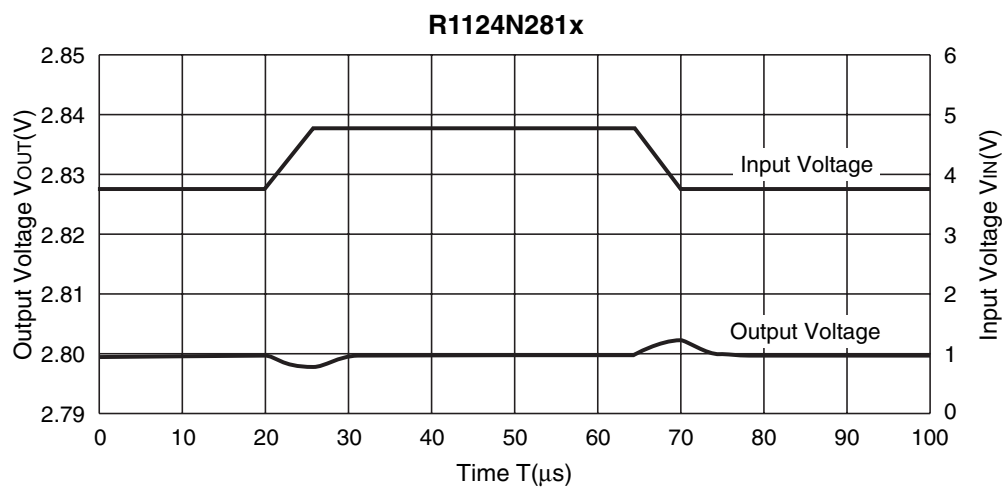
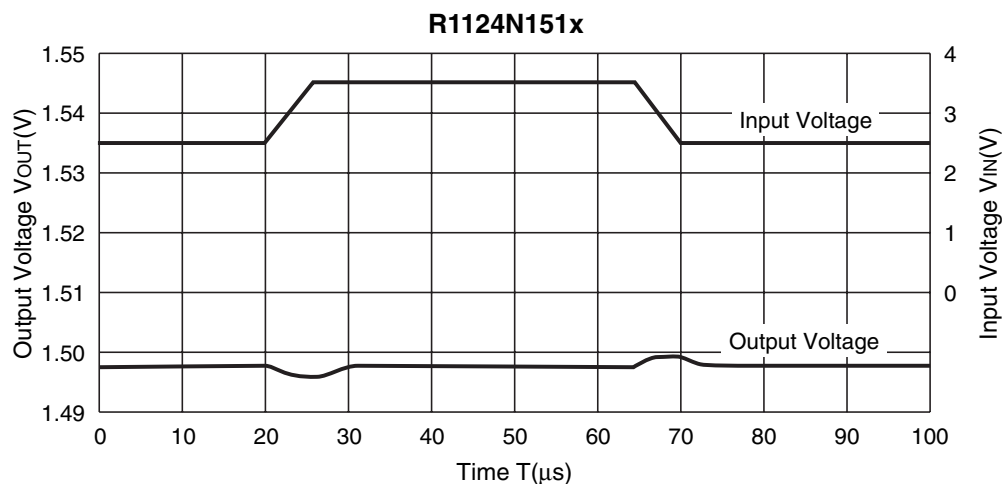


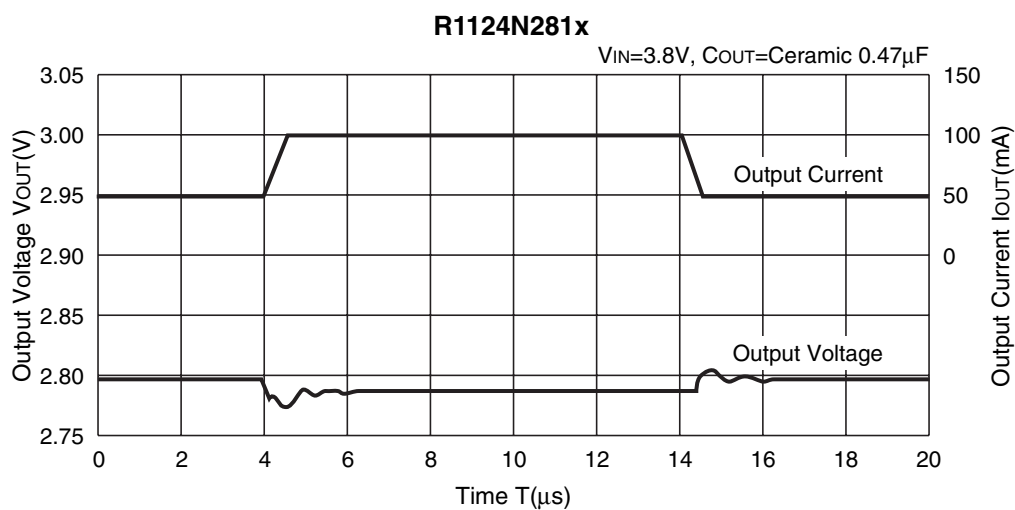
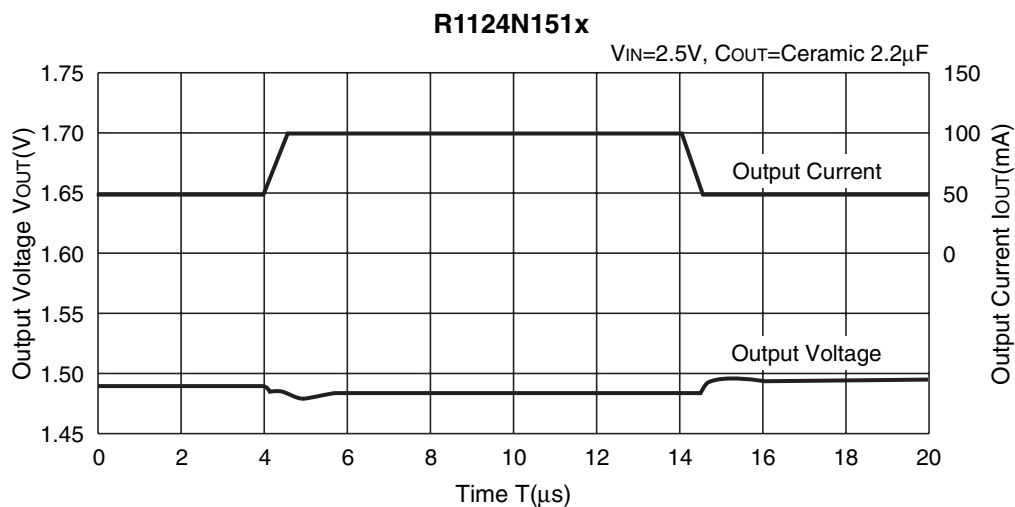
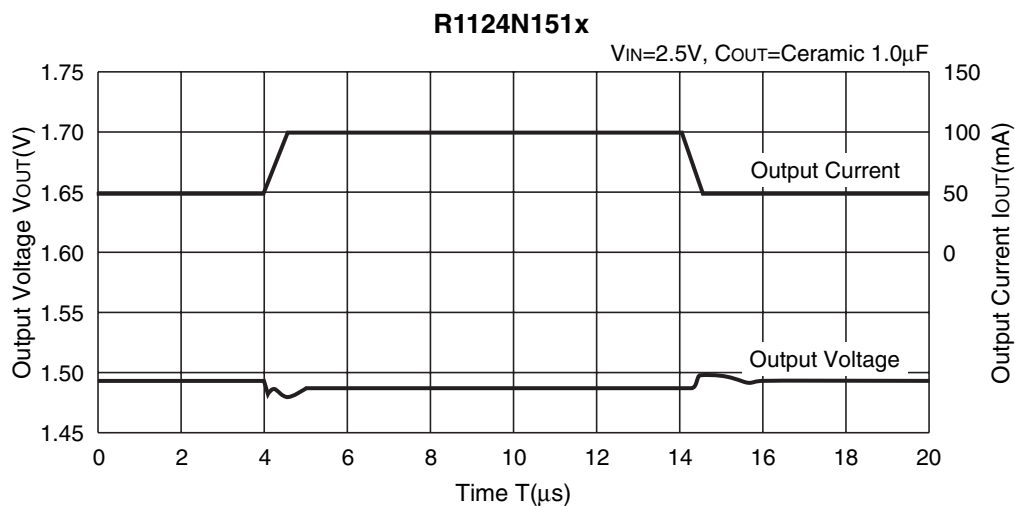
9) Ripple Rejection vs. Frequency ($C_{IN}=\text{none}$)

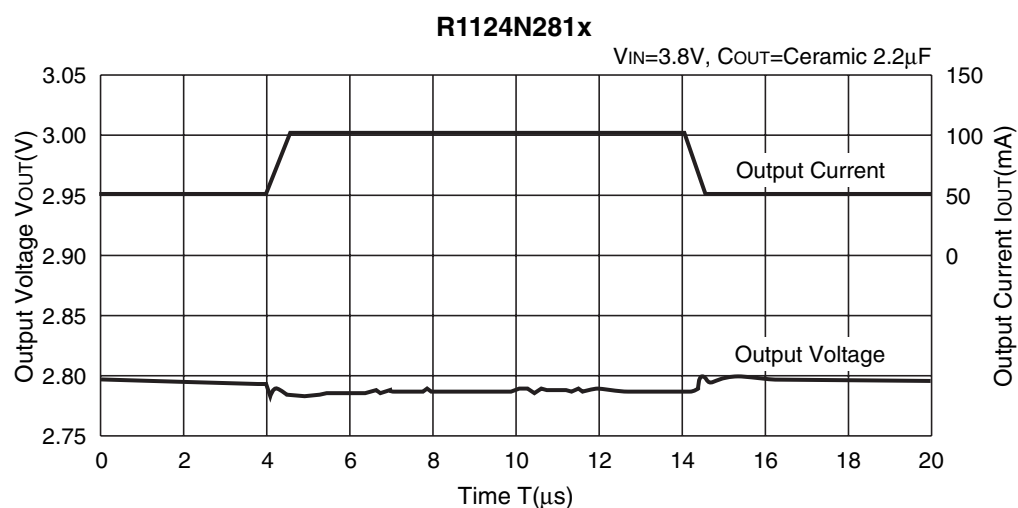
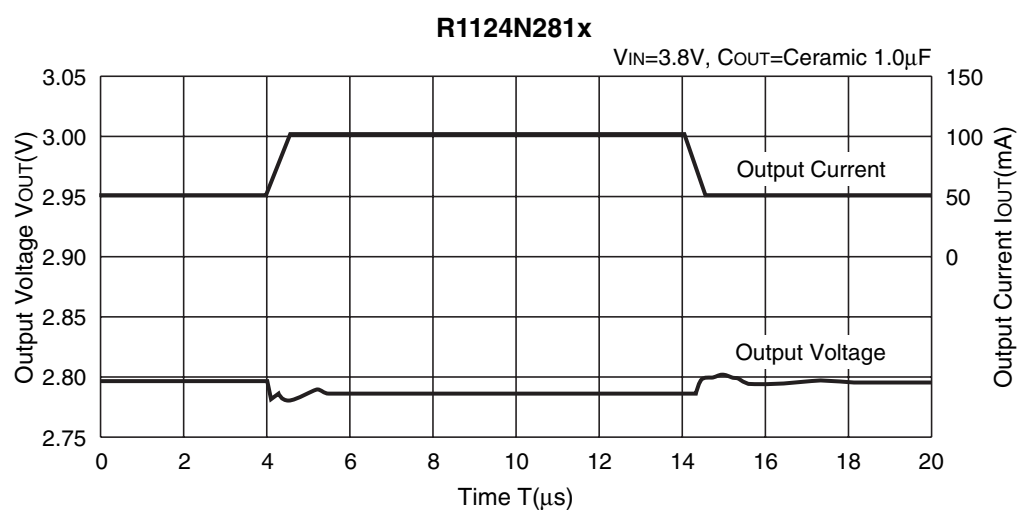




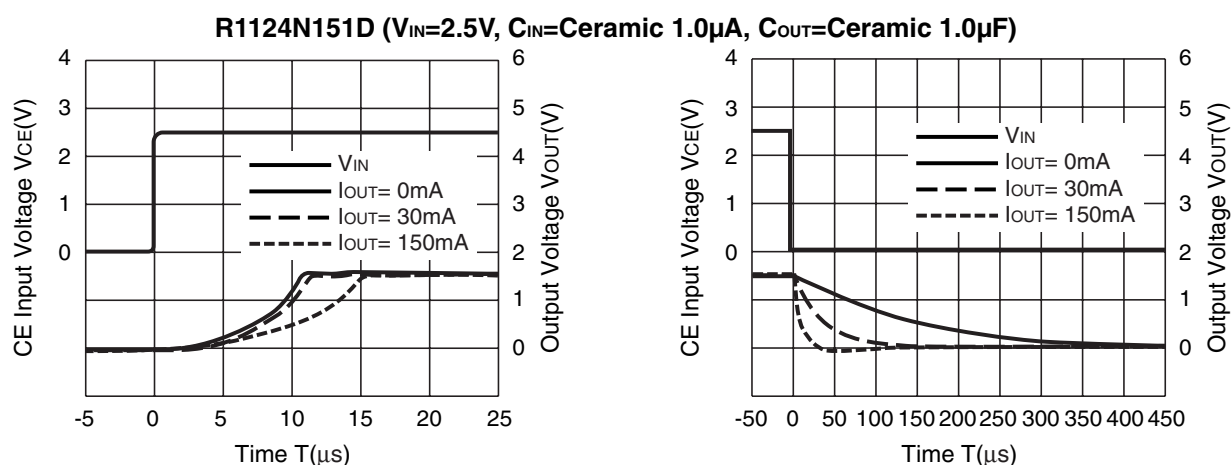
10) Input Transient Response ($I_{OUT}=30mA$, $C_{IN}=\text{none}$, $t_r=t_f=5\mu s$, $C_{OUT}=\text{Ceramic } 0.47\mu F$)

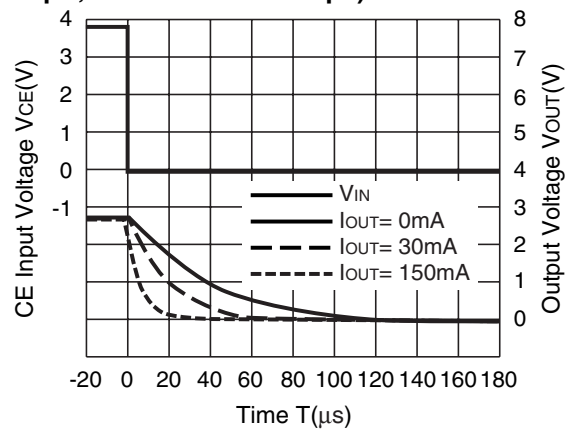
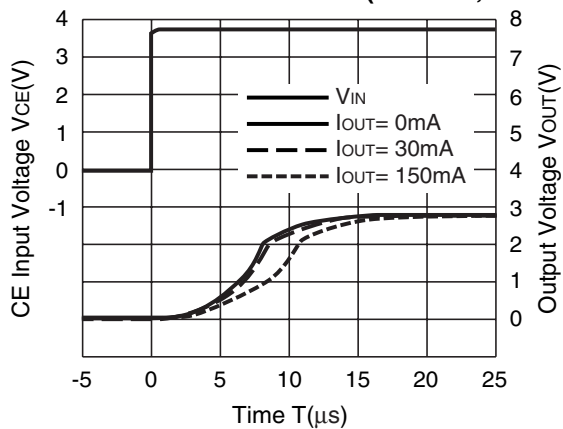
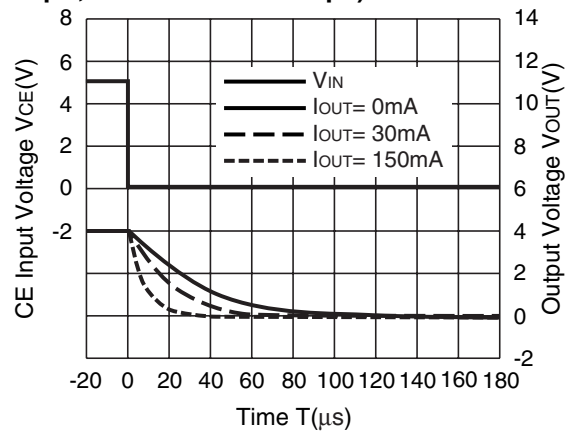
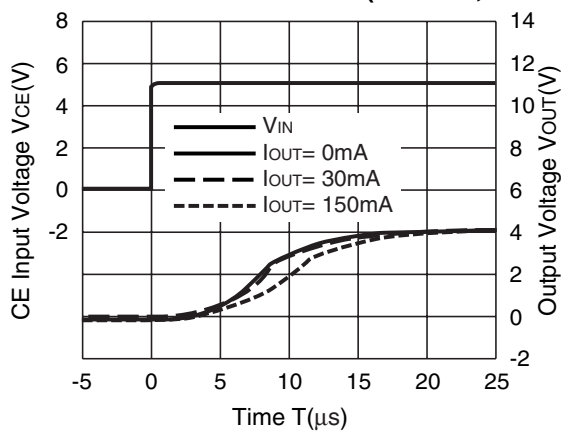


11) Load Transient Response ($t_r=t_f=0.5\mu s$, C_{IN} =Ceramic $1.0\mu F$)



12) Turn-on/off speed with CE pin (D version)

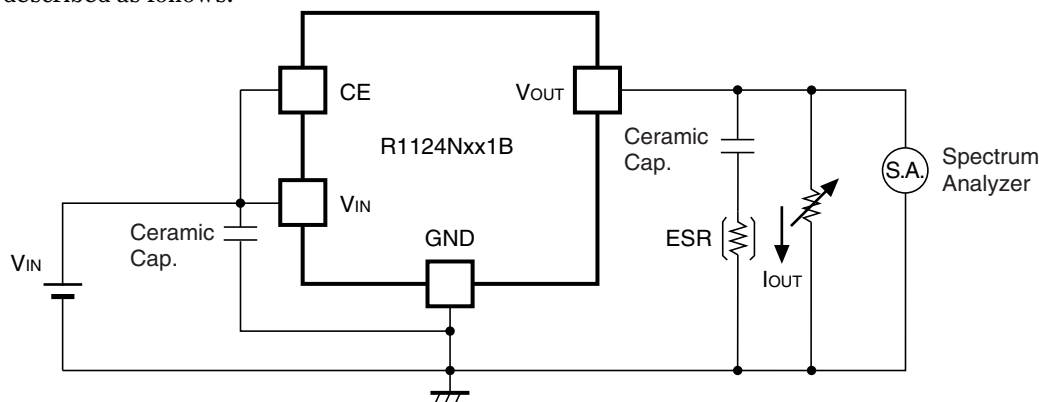


R1124N281D ($V_{IN}=3.8V$, $C_{IN}=\text{Ceramic } 0.47\mu A$, $C_{OUT}=\text{Ceramic } 0.47\mu F$)R1124N401D ($V_{IN}=5.0V$, $C_{IN}=\text{Ceramic } 0.47\mu A$, $C_{OUT}=\text{Ceramic } 0.47\mu F$)

TECHNICAL NOTES

When using these ICs, consider the following points:

In these ICs, phase compensation is made for securing stable operation even if the load current is varied. For this purpose, use a capacitor C_{OUT} with good frequency characteristics and ESR (Equivalent Series Resistance) of which is in the range described as follows:



Measuring Circuit for white noise; R1124Nxx1B

The relations between I_{OUT} (Output Current) and ESR of an output capacitor are shown below. The conditions when the white noise level is under $40\mu V$ (Avg.) are marked as the hatched area in the graphs.

(Note: If additional ceramic capacitors are connected to the Output Pin with Output capacitor for phase compensation, the operation might be unstable. Because of this, test these ICs with as same external components as ones to be used on the PCB.)

<Measurement conditions>

- (1) $V_{IN} = V_{OUT} + 1V$
- (2) Frequency Band: 10Hz to 2MHz
- (3) Temperature: $-40^{\circ}C$ to $85^{\circ}C$

